

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. PATENT DATE
 SCANNED gk o.k. [Signature]

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| APPLICATION NO. 09/557640 | CONT/PRIOR D | CLASS 370 | SUBCLASS 537 | ART UNIT 2739 2738 | EXAMINER <i>F. A. O.</i> |
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ANTS William Dally
John Foulton


TRAN, T

Clock multiplying delay-locked loop for data communications

PTO-2040
12/99

| ISSUING CLASSIFICATION | | | | | | | | | | | | |
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| ORIGINAL | | | | CROSS REFERENCE(S) | | | | | | | | |
| CLASS | | SUBCLASS | | CLASS | SUBCLASS (ONE SUBCLASS PER BLOCK) | | | | | | | |
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| INTERNATIONAL CLASSIFICATION | | | | | | | | | | | | |
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|  TERMINAL DISCLAIMER | DRAWINGS | | | CLAIMS ALLOWED | |
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims | Print Claim for O.G. |
| <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. | _____ (Assistant Examiner) (Date) | | | NOTICE OF ALLOWANCE MAILED | |
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| <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____ | _____ (Primary Examiner) (Date) | | | ISSUE FEE | |
| | | | | Amount Due | Date Paid |
| <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed. | _____ (Legal Instruments Examiner) (Date) | | | ISSUE BATCH NUMBER | |
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